

SKN SINHAGAD COLLEGE OF ENGINEERING

E&TC Engineering Department

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ELECTRONICS AND TELECOMMU-NICATION ENGINEERING DEPART-MENT ORGANIZED TECHNICAL ACTIVITIES STUDENTS PARTICI-PATED IN THE ACTIVITES WITH FULL SPIRIT.

INSIDE THIS ISSUE:

- "Advances in Embedded System"
- "Internet of things 2 and its applications"
- "CMOS Analog
 Circuit Design"
- "Programming and Customizing using AVR Controller"
- "C Programming" 3
- "Industrial Automation: Special Purpose Machine Design"
- "Virtual Campus"
- "University Rank- 4 ers"

Webinar on "VLSI Design using Verilog HDL"

With full enthusiasm free webinar was organized by ETELSA, Department of E&TC, SKN Sinhgad College of Engineering, Korti, Pandharpur on 17th July, 2019, Wednesday. The event has started at 2:00 pm by Mr P R Sivakumar — Founder & CEO, Maven Silicon who has 20+ years of experience in Semiconductor Industry. Agenda of workshop was overview of VLSI Design (IPs, Chips and SoCs, SoC Design, ASIC Vs FPGA), RTL Design using Verilog HDL (Overview of Verilog, Verilog



Language, Basics and Constructs and Verilog Abstraction Levels), Data Types (Data type concepts, Nets and Registers, Non hardware equivalent variables Arrays) and Verilog Operators (Logical operators, Bitwise and Reduction operators, Concatenation and Conditional Relational, Arithmetic Shift and Equality operators and Operator precedence). He also discussed various opportunities available in VLSI field.

Our Pride

Department of E&TC of SKNSCOE always took efforts for the placement of the students in reputed organizations. Prof. H. S. Deshpande, TPO coordinator, always takes rigorous follow up of students, their preparation and involvement in various placement activities.



Ghadage Dattatray Cognizant, 4 LPA



Gadage Prajakta Zensoft, 3.5 LPA



П

Rokade Poonam Zensoft, 3.5 LPA



Dr. A. O. Mulani, Dean Quality Assurance (NAAC & NBA) is awarded with Ph. D. from Pune University in June 2019.

Seminar on "Advances in Embedded System"



A seminar was organized under ETELSA on **22nd Aug. 2019**. Prof. P. B. Borole, from VJTI College Matunga Mumbai. Once an embedded hardware is programmed for a certain task, it is used forever for the same task. Changing the firmware afterwards is not possible. Such systems are limited in computational resources like memory, CPU processing speed, I/O facilities but are still capable of performing the task given to them very

efficiently. Because of RTOS we are able to complete Mars mission. In Mars rover NASA has used Vx-Works RTOS. IoT has made everything Automated like Home Automation. Because of Embedded Linux we can modify Linux kernel according to our need and which will help us in maintaining Process priorities.

Three days' workshop on

"Internet of Things and its Applications"



Internet of Things or IoT in short, is the idea of making devices and objects smarter by linking them to the internet. This workshop has focused on the amazing world of IoT and its applications. It enhances the knowledge of students on importance of IoT and how they can use it for their betterment and project purpose. Workshop was held on **4-6 Oct. 2019.**

Workshop Outcome are Learn the basics of IoT and its applications, Build your IoT projects using node mcu, Work with different sensors to detect different parameters., Setup IoT connectivity using a remote desktop, Understand node mcu programming and API, Develop and test an IoT weather monitoring station.

Guest Lecture on, "CMOS Analog Circuit Design"

The guest lecture was arranged on **05 August 2019**. The Resource person Dr B. P. Ladgaonkar, Principal, Ganpatrao Arwade College of Commerce, Sangli.

Prof. S.V.Surwase, HOD ENTC Dept felicitated Dr. B. P. Ladgaonkar. The resource person took over the session at 12:35 PM discussing overview, Introduction to semiconductor material, silicon & germanium semiconductor, doping concept, formation of P-N junction, concept of depletion layer ,basics of transistor ,MOSFET and FET transistor. He also suggested the future scope and research avenues availed in this domain.



One Day Workshop on "Programming and Customizing using **AVR Controller**"

The function has started by the felicitation of Guests, Dr. Prof. Suhas Patil, T. C. College Baramati. Ms. Girija Damodare introduced guests & expressed her views about the program. Prof. A. C. Pise, Workshop coordinator, emphasized on benefits of student's participation in such event to develop technical knowledge required for industries. Workshop was held on 20 Aug 2019.

Workshop mainly focuses on the students eager to learn about



Advance features of AVR Microcontroller. They will get a chance to expand their knowledge in interfacing external advance peripherals like Matrix Keypad, LCD Display, Seven Segment Display and their application in real time projects. Students gained knowledge of AVR Microcontroller, Advanced Embedded C programming Knowledge of Sensors Programming.

One Day Workshop on "C Programming"



With full enthusiasm a one day workshop Innovation Forum was organized by Department of Electronics & Telecommunication Engineering, Sinhgad College of Engineering, Korti, Pandharpur on 20th Sept 2019. The function was coordinated and presided over by Prof. A. B. Jagadale, Associate Professor and Prof. G.V. Linge Department of E&TC SKNSCOE Korti. The func- 📗 nical knowledge. tion has started by the Introduction of C programming by Prof. | The students have introduced with various fundamental con-A.B.Jagadale. The students have introduced with various fundamental concepts and programming practices by Prof. G.V. Ling of C programming, data types, programming structure etc.

Guest Lecture on "Industrial Automation Special Purpose Machine Design"

To enhance knowledge and motivate students guest was organized by Department of Electronics & Telecommunication Engineering, Sinhgad College of Engineering, Korti, Pandharpur on 24th July 2019. The lecture was given by Mr. Navnat Takle, Success Industries PVT LTD Pune and coordinated by Prof. A. B. Jagadale, Associate Professor Department of **E&TC SKNSCOE Korti.**

The function has started by the Introduction of industrial automation by Mr. Takale. He also emphasized on benefits to students by participation in such developments to excel tech-

cepts industrial automation, PLC, SCADA and DCS. He also presented and explained industrial project development process. Recordings of SPM developed have been demonstrated to the students for more visualization.

One page Report on "Virtual Campus"

Industry interaction should continue to remain the sole responsibility of the Institution. In other words each Institution will continue to be responsible & accountable for the placement of its students, for which they will perform all activities including liaison with Corporate and companies, Accreditation of their Institutions with corporate & companies, etc. Placement is the sole responsibility of students. The Training & Placement cell & various other faculty members only facilitate & provide the guidance to the students.

The Event Virtual Campus was organized by ETELSA under the department of E&TC on Saturday **28 th September.** It is preceded by lamp lightening ceremony by guests. It had three rounds: Aptitude + GD + Interviews.



All the rounds were conducted by industrial experts. First day Aptitude is conducted for total 195 students. Out of 195 students, shortlisted students appeared for GD. GD shortlisted students were appeared for Interviews. Function was ended by valedictory function where winners of each round are awarded by medal, cash prize and appreciation certificate.

Heartiest Congratulations to University Rankers

Magar Kajal SY Btoch 7 th Rank

Damodare Girija

TY Btech 2nd Rank

Sutar Apurva

Mtoch J 2nd Rank













More Jyoti Mtech I 3rd Rank

Mitkal Pooja Mtech I 4th Rank

Rgut Sonahi Mtech I 5th Rank